Terminal CPX

Technical data – Interlinking block with system supply
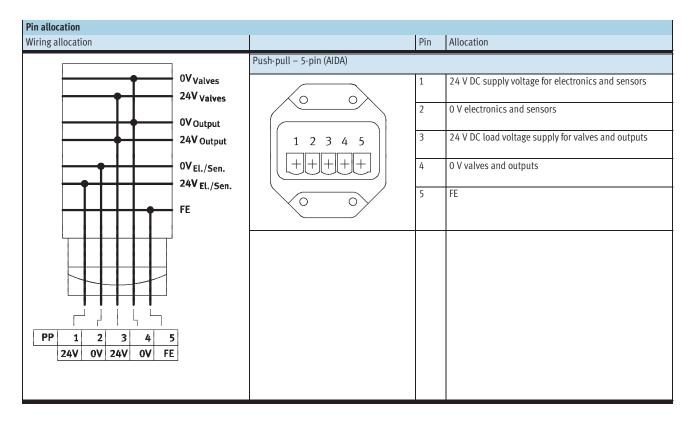
Pin allocation

Wiring allocation		Pin	Allocation
	M18 – 4-pin	1	24 V DC supply voltage for electronics and sensors
		2	24 V DC load voltage supply for valves and outputs
		3	0 V
		4	FE
	7/8" – 4-pin	A	24 V DC supply voltage for electronics and sensors
		B	24 V DC load voltage supply for valves and outputs
		C	FE
		D	0V

M18	1	2	3	4
7/8"	A	B	D	C
	24V	24V	0V	FE

Pin allocation

Wiring allocation		Pin	Allocation
	7/8" – 5-pin	1	0 V valves and outputs
		2	0 V electronics and sensors
		3	FE
		4	24 V DC supply voltage for electronics and sensors
		5	24 V DC load voltage supply for valves and outputs

7/8"	1	2	3	4	5
	0V	0V	FE	24V	24V

Terminal CPX

FESTO

Technical data – Interlinking block with system supply

Pin allocation				
Wiring allocation			**Pin**	**Allocation**
		Push-pull – 5-pin (AIDA)		
			1	24 V DC supply voltage for electronics and sensors
			2	0 V electronics and sensors
			3	24 V DC load voltage supply for valves and outputs
			4	0 V valves and outputs
			5	FE

Wiring diagram labels:

0V Valves
24V Valves
0V Output
24V Output
0V El./Sen.
24V El./Sen.
FE

PP	1	2	3	4	5
	24V	0V	24V	0V	FE

Push-pull connector: 1 2 3 4 5